

a plurality of data lines, electrically insulated from said plurality of gate lines, formed on said first substrate such that said plurality of data lines intersect with said plurality of gate lines to define cell areas;

a pixel electrode formed in each of said cell areas;

A  
a transistor associated with each pixel electrode, said transistor having a gate, source and drain, said drain connected to said pixel electrode, said source connected to one of said plurality of data lines, and said gate connected to one of said plurality of gate lines;

barrier ribs formed on said source and drain of each transistor;

an ink-jet color filter formed in each cell area between said barrier ribs;

a protective layer formed in a portion of each cell area; and wherein

a portion of said ink-jet color filter formed in each cell area is formed on said protective layer.

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Attached hereto is a marked-up version of the changes made to the application by this Amendment.